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THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Ference et al. Confirmation No.: 9501
Serial No.: 09/732,240 Group Art Unit: 2815
Filed: 12/07/00 Examiner: Chris C. Chu
Title: MULTI-CHIP STACK AND METHOD OF FABRICATION
UTILIZING SELF-ALIGNING ELECTRICAL CONTACT ARRAY

Certificate of Mailing

I hereby certify that this correspondence is being deposited with the U.S. Postal Service as first class mail in an envelope addressed to: Box Non-Fee Amendment, Assistant Commissioner for Patents, Washington, D.C. 20231, on July 09, 2002.

Kevin P. Radigan
Kevin P. Radigan
Attorney for Applicant
Reg. No. 31,789

Date of Signature: July 09, 2002

Box Non-Fee Amendment
Assistant Commissioner for Patents
Washington, D.C. 20231

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TECHNOLOGY CENTER 2800

Response to Office Action

Dear Sir:

This paper is submitted in reply to the Office Action mailed April 09, 2002 in connection with the above-designated application. Allowance of all claims is respectfully requested in view of the amendments and remarks below.